



Figure 1

Upload IP MetaData

| Identification Data | |
|---------------------|------------------------------|
| Category | Networking and Communication |
| Block Name | |
| Availability | (yyyy/mm/dd) |
| Technology | Unspecified |
| Key Features | |
| | |
| Data Sheet | http:// |
| Claims | |
| Frequency | Hz |

Figure 2

| Identification Data | |
|---|---|
| Category | Networking and Communication |
| Block Name | ARM10_Thumb - 300MHz_low-power |
| Availability | 01/01/2001 (yyyy/mm/dd) |
| Technology | ASIC |
| Key Features | |
| The ARM10T processor is designed to deliver 400 Dhrystone 2.1 MIPS at 300 MHz, and features an optional Vector Floating-Point unit capable of delivering 600 MFLOPS. This level | |
| Data Sheet | http://www.arm.com/sitearchitek/arm |
| Claims | |
| Frequency | 300000000 Hz |

Figure 3



ADMINISTRATION

Registration

► Corporate Registration

► User Registration

Access Management

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CATALOG BUILDING

Taxonomy

Format Library

► Build a Format

► Modify a Format

► Remove a format

Catalog

► Assign a Format to a Taxonomy Node

► Disable a Format for Taxonomy Node

Home Page

Exit

10

10

13

Figure 4

Design And Reuse Encapsulation Portal

Administration Upload Search Contact Us Online Help

Upload IP Metadata

Build the infrastructure By Keyword Id 18

11 top node

11 Corp A

- Physical Library
- Memory Element
- Analog and Mixed

11 Corp B

- Bus Interface/Periph
- CODEC/Encryption
- Networking and Con
- Graphics/Image/Auc

| | | |
|---------------------|---------------|----|
| Upload IP Metadata | Communication | 14 |
| Modify IP Metadata | | 15 |
| Delete IP Metadata | | 15 |
| Release IP Metadata | | 16 |
| Send IPs to Corp A | | 17 |

Availability (yyyy/mm/dd)

Technology Unspecified

Key Features

Data Sheet http://

Deliverables

Figure 5